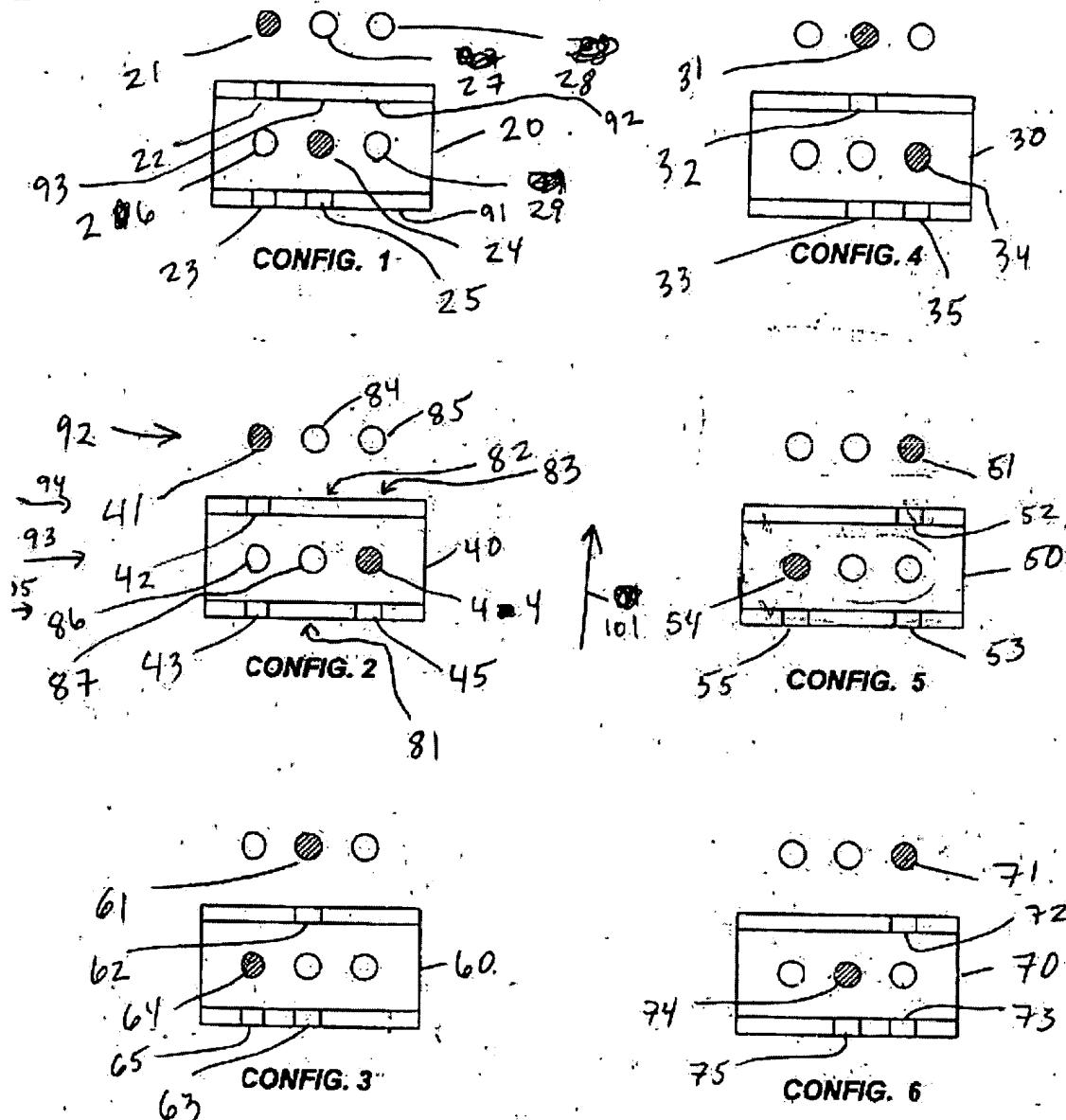


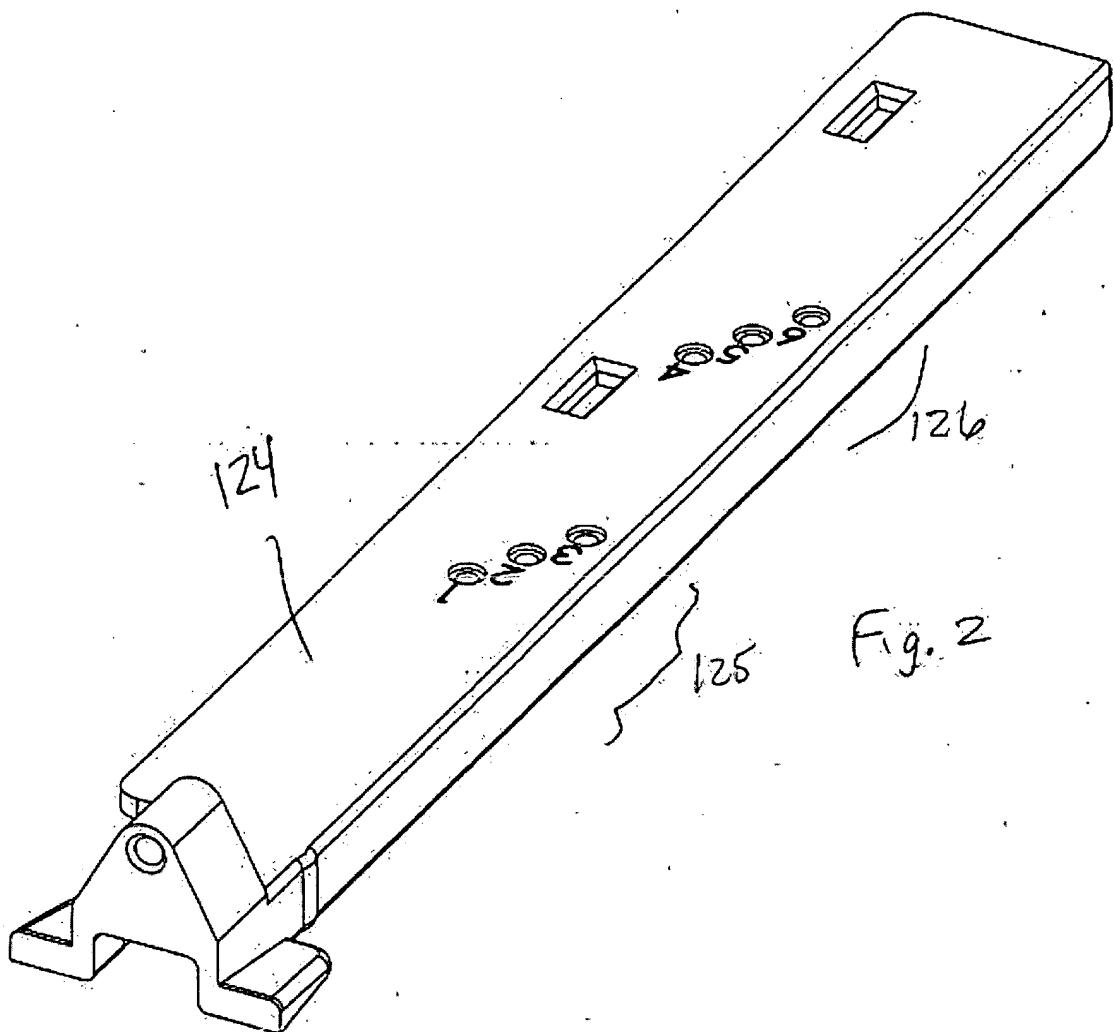
perpendicular thereto. -

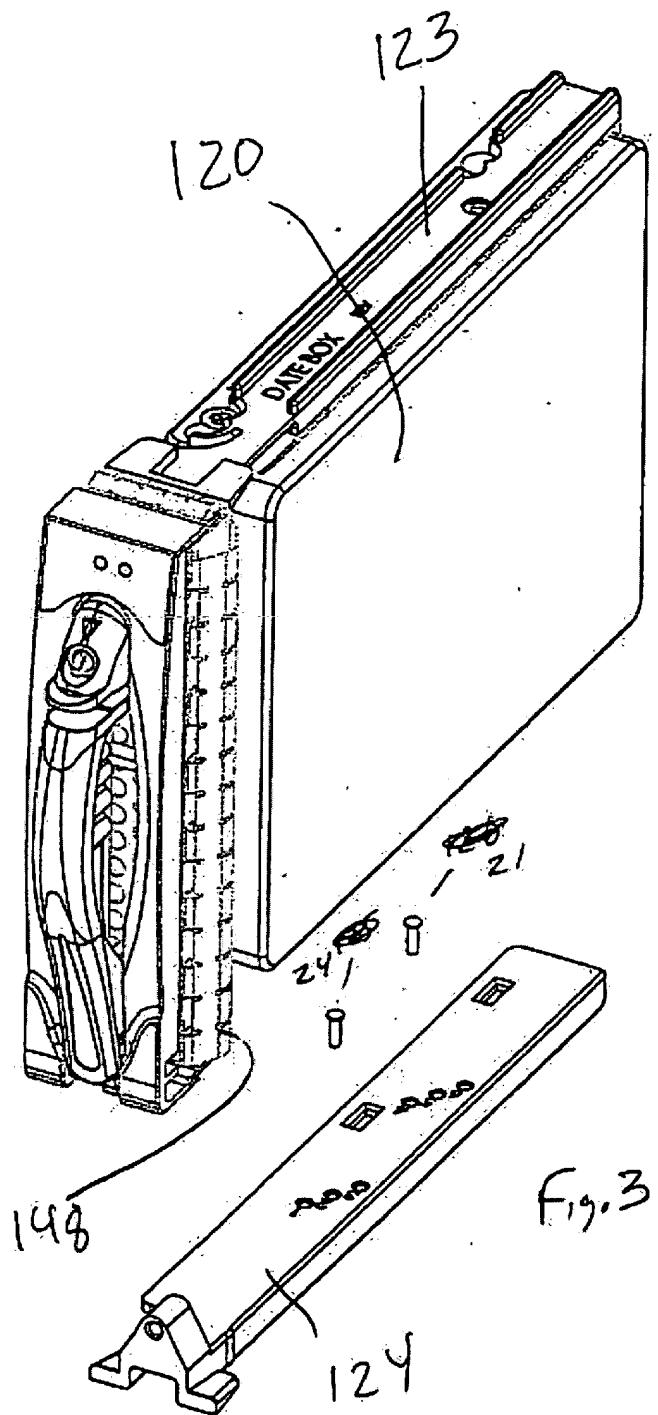
## Figures

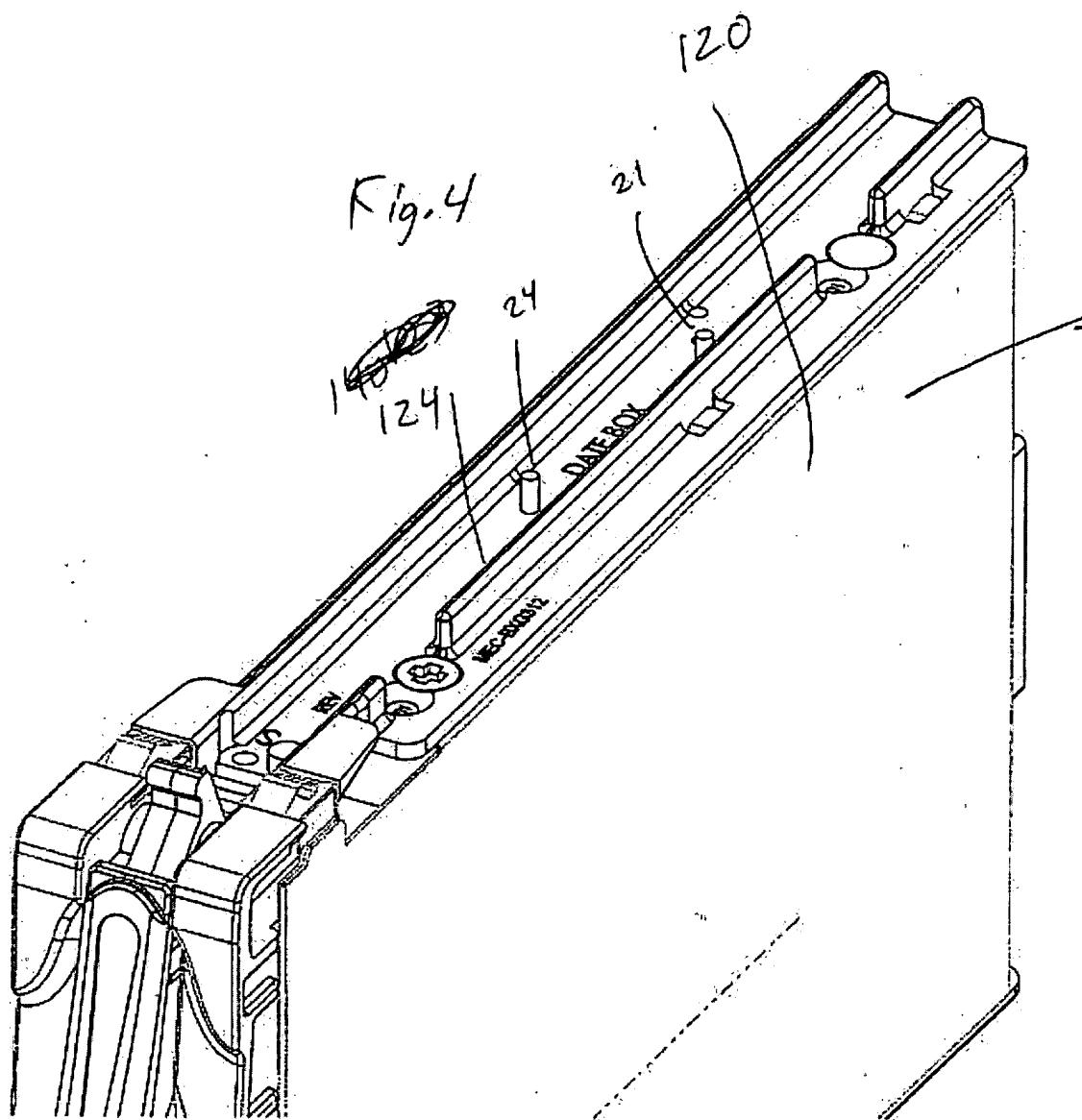


KEYING SCHEMATIC FOR 2X3 PIN LAYOUT  
(6 POSSIBLE CONFIGURATIONS)

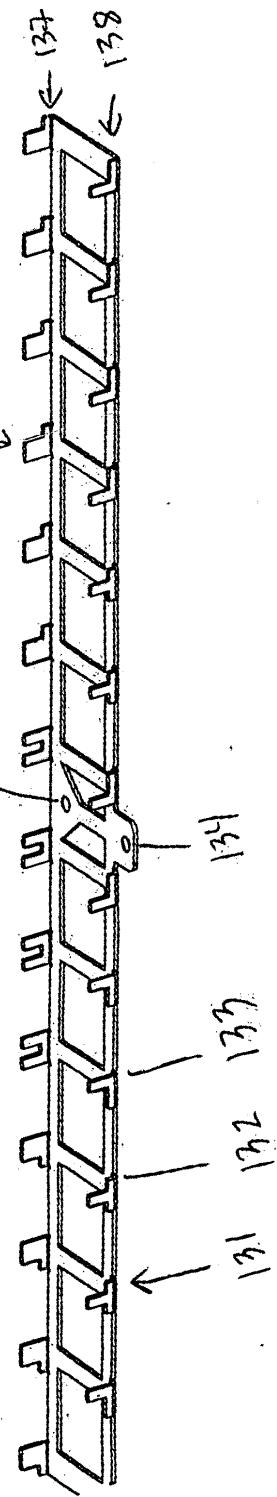
Fig. 1







SPECIFICATION



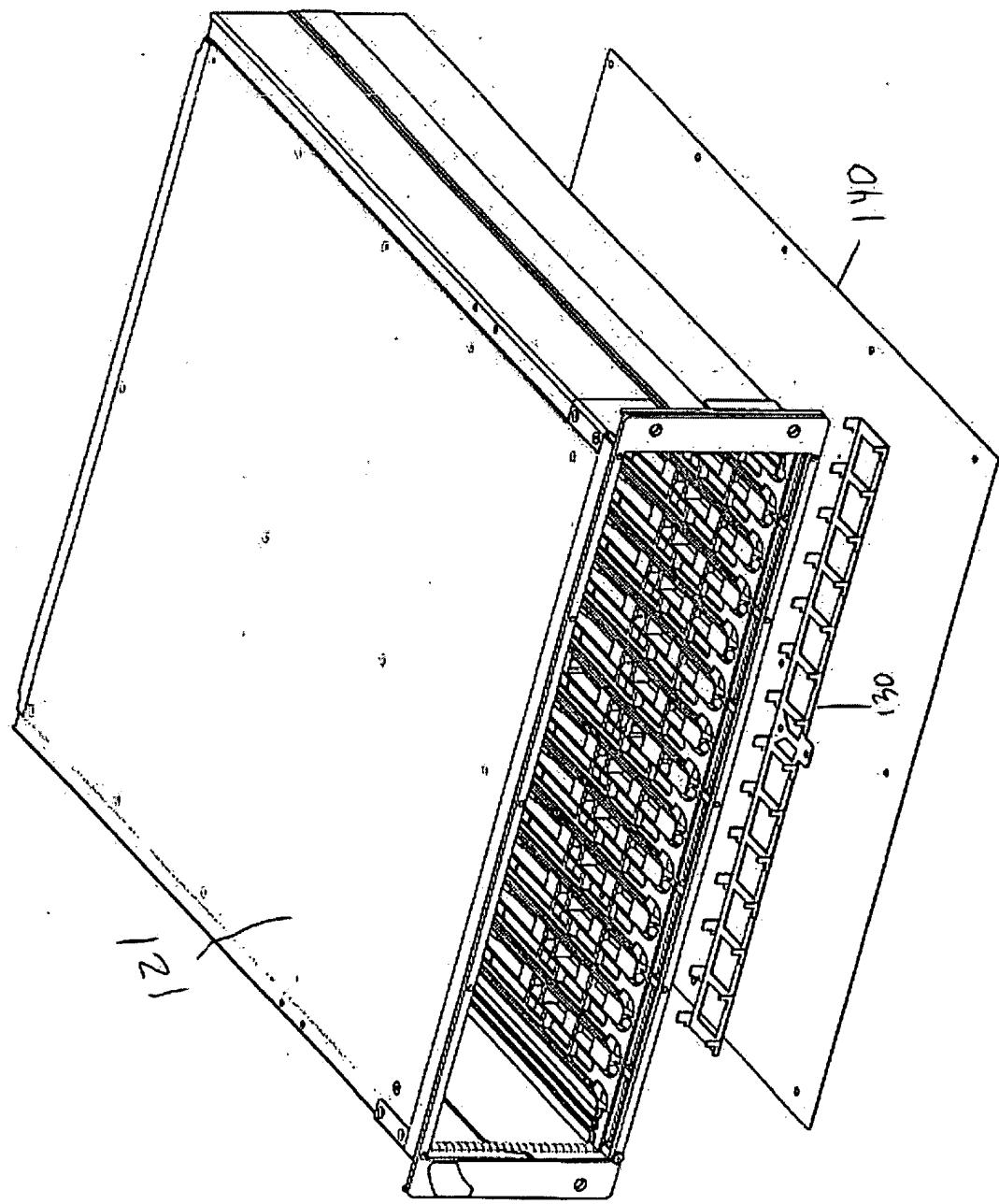
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2/2/2002 1:36 PM

LOGGING - 42 - GUIDE

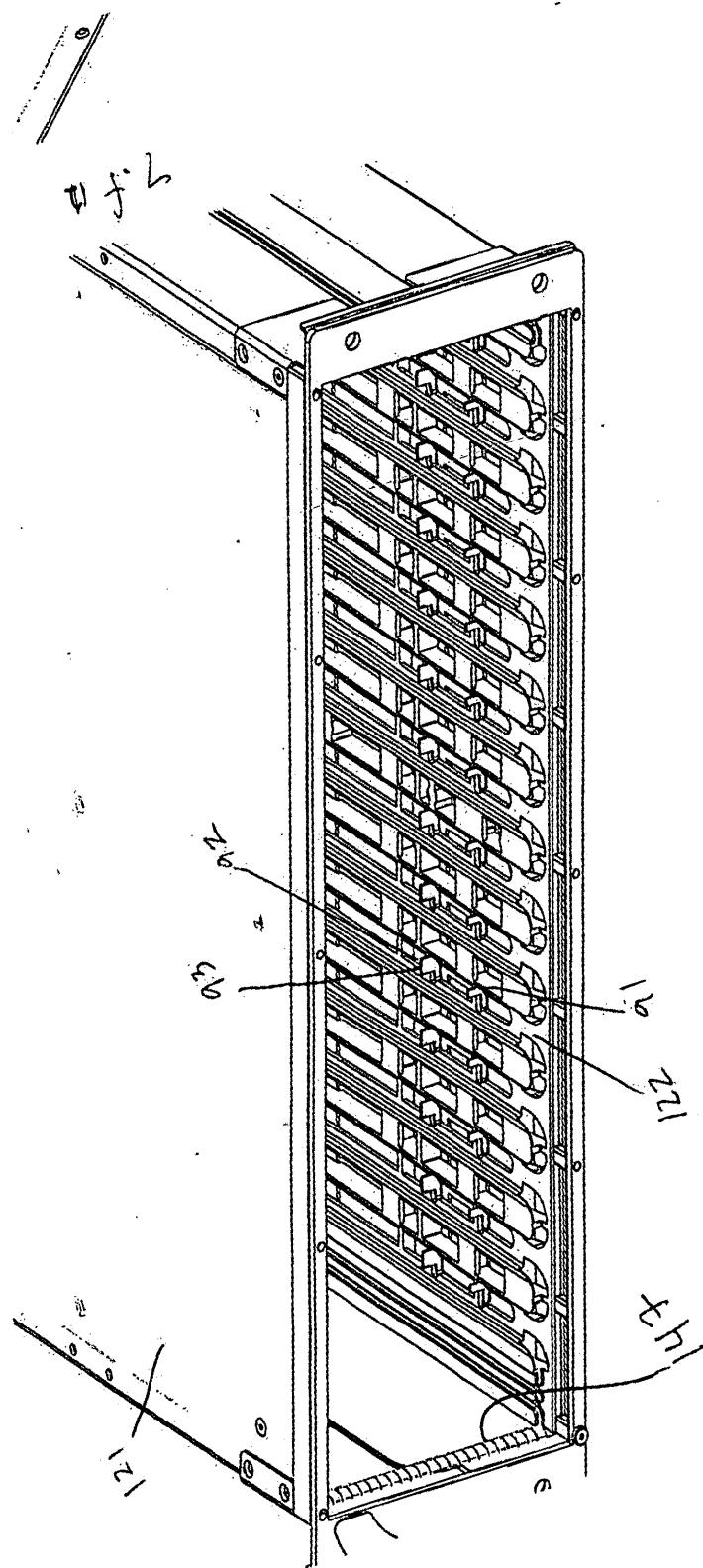
<https://secure-patents.com/erolgy/p021/application.htm>

6 of 12



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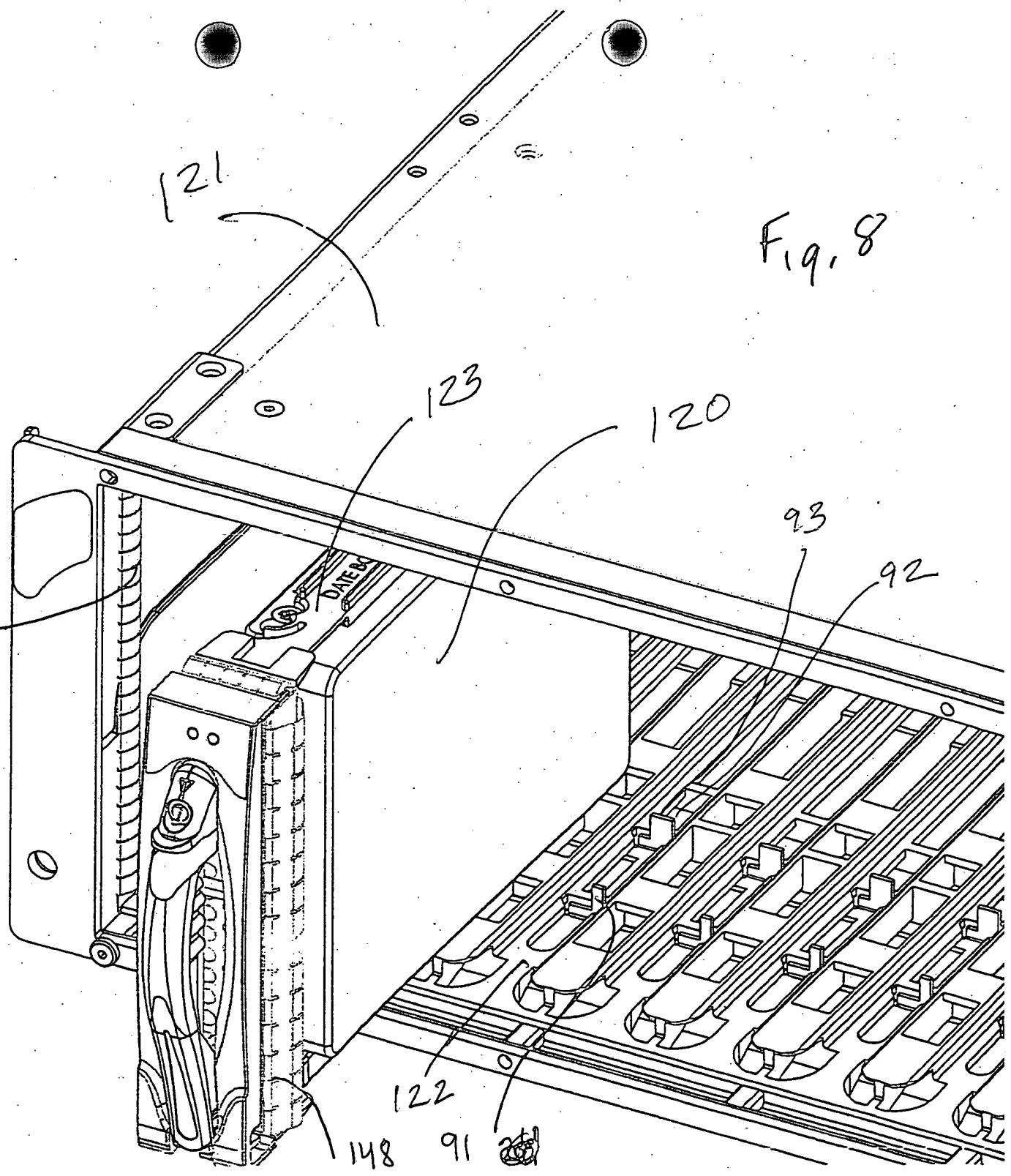
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## SPECIFICATION

<https://secure.parentscouncil.org/cp021/application.htm>

Fig. 8



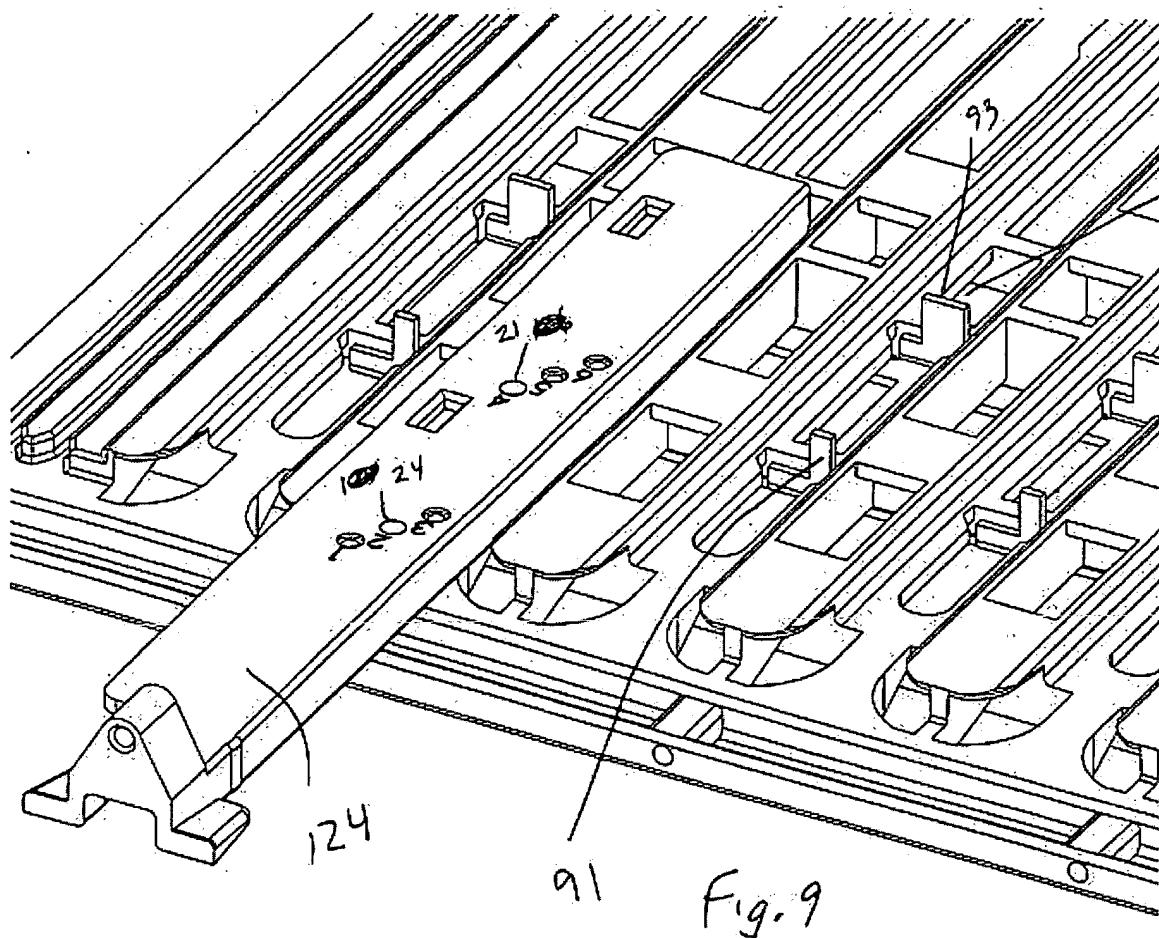
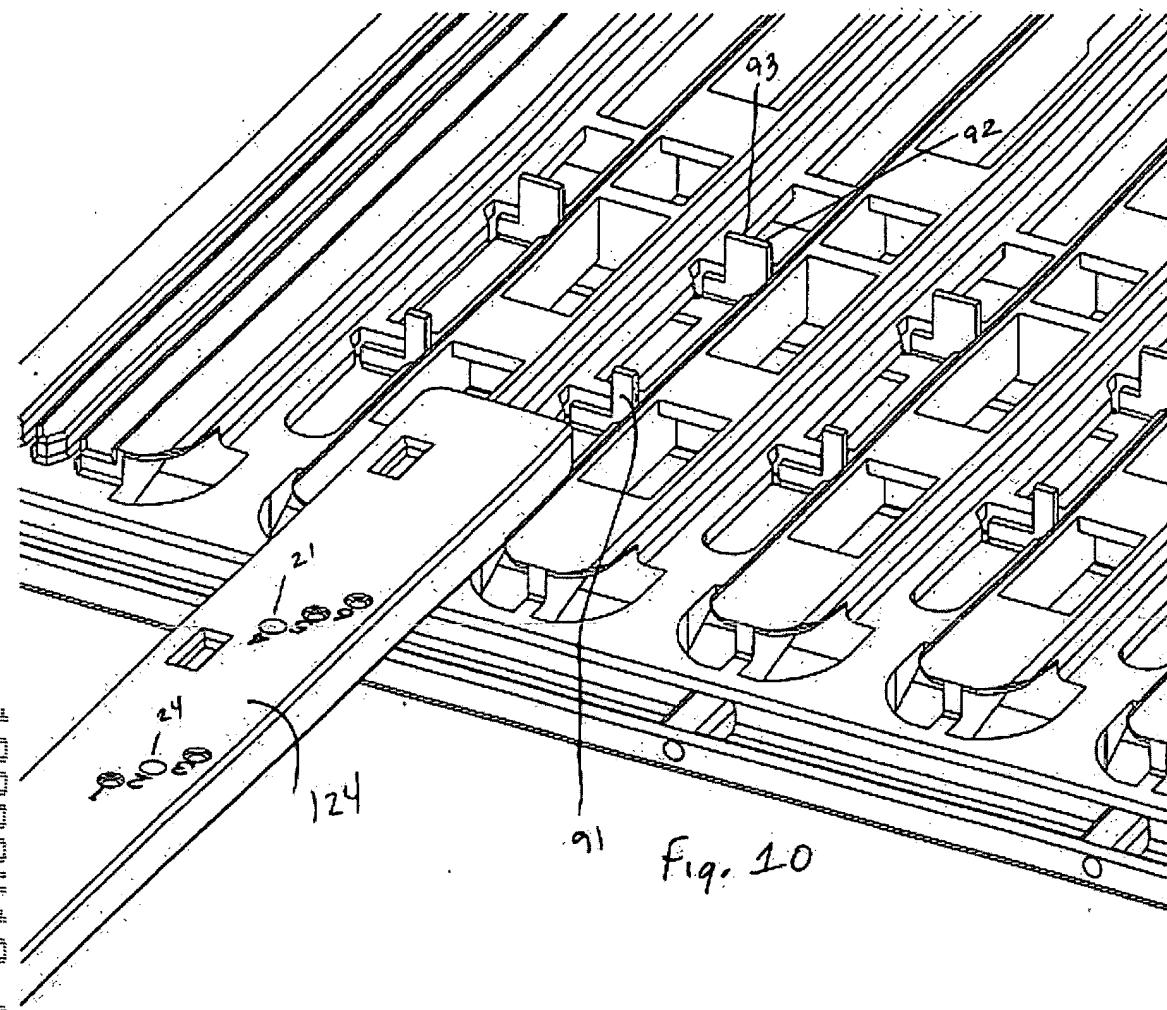
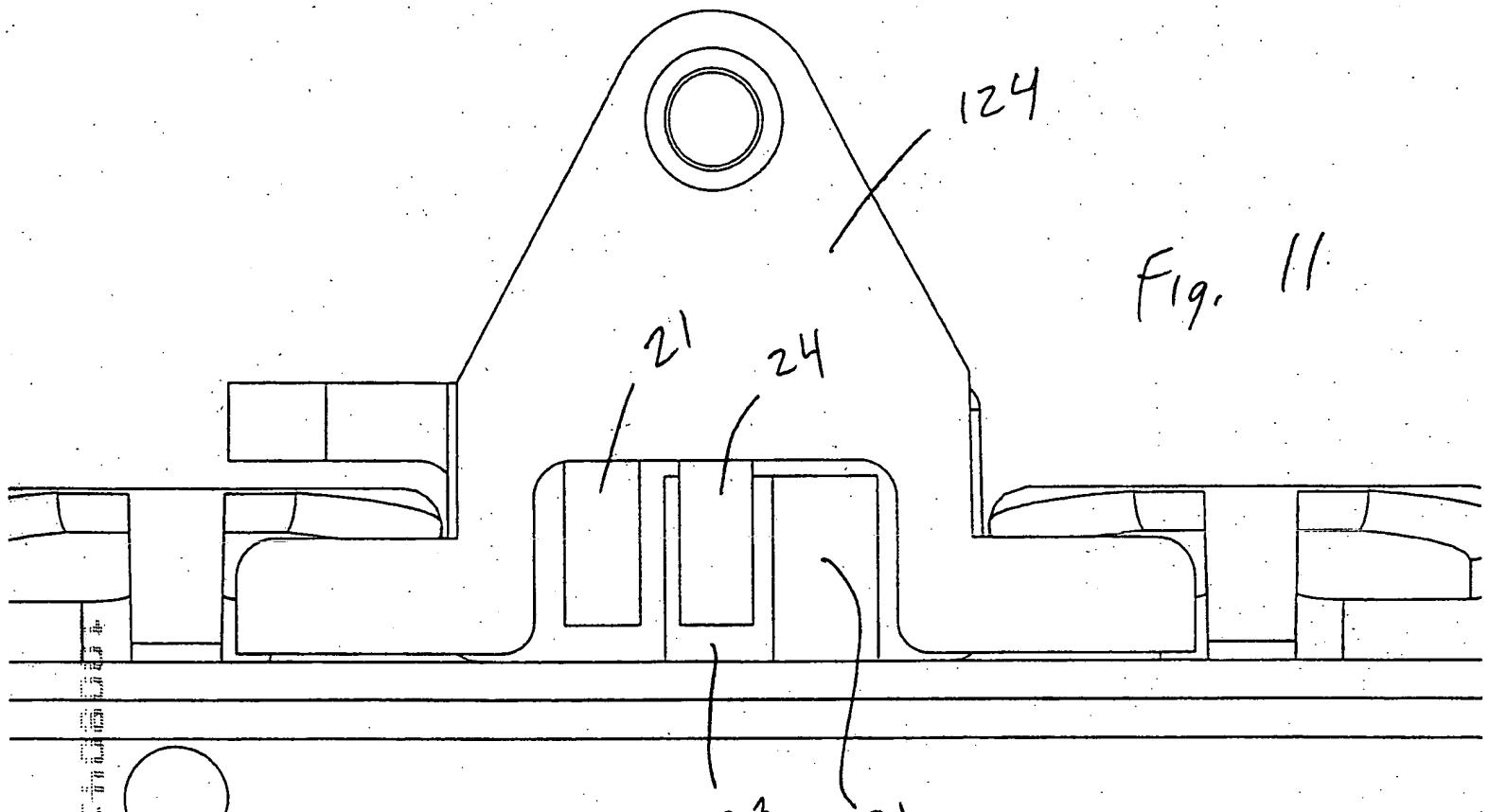


Fig. 9





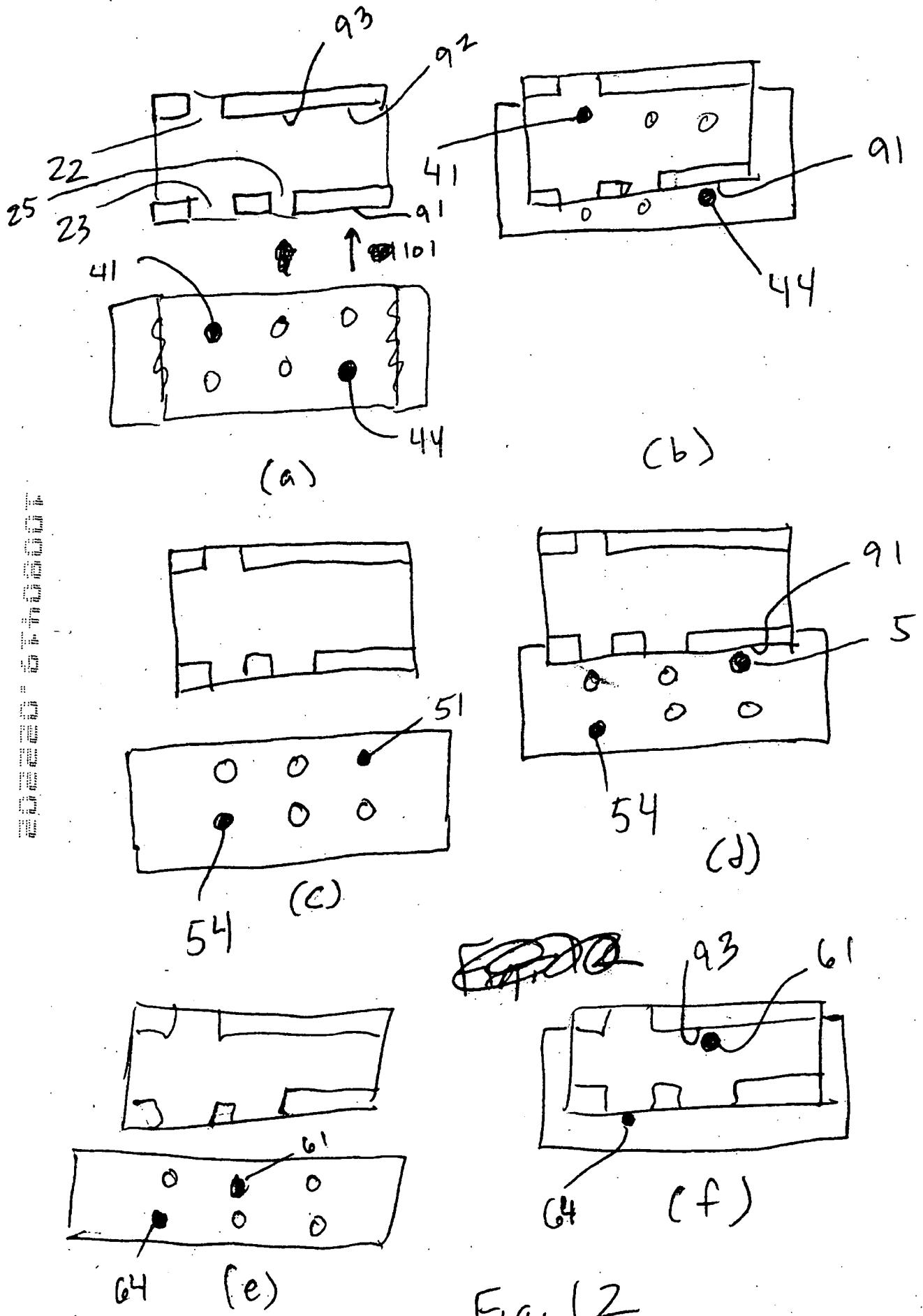


Fig. 12